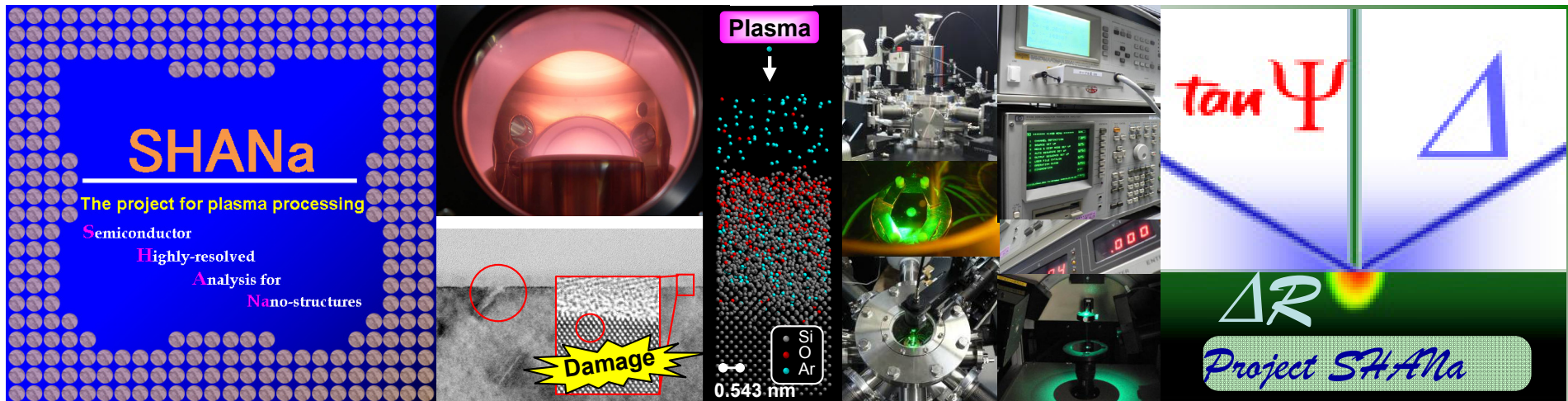


Plasma-Induced Damage in 3D Structures behind Device Scaling

K. Eriguchi, Y. Takao, and K. Ono
 Kyoto University, JAPAN

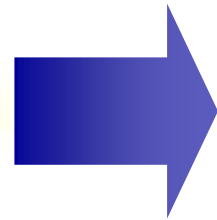
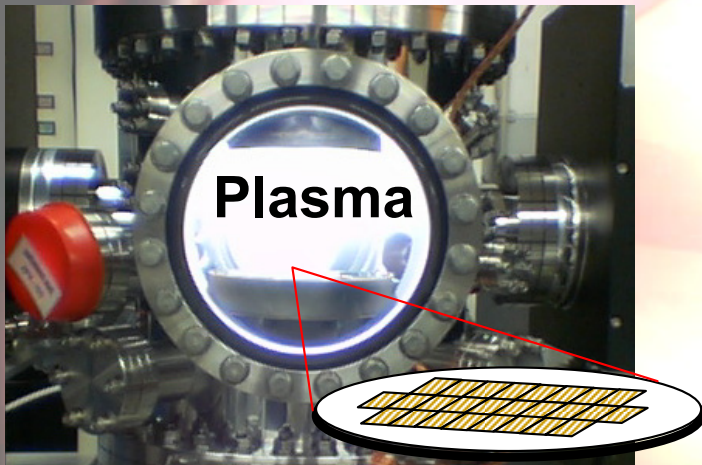
Acknowledgements: This work was partly supported by JSPS and STARC project in Japan.



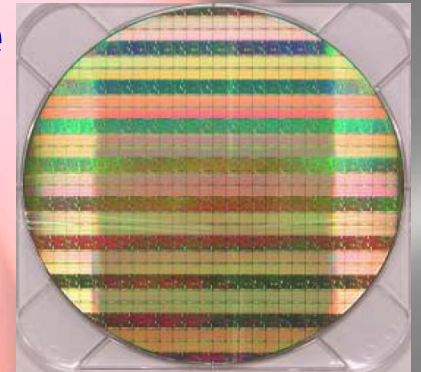
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- 1. Introduction – Plasma-Induced Damage (PID)**
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 - 2-3 Electronic State of Defect**
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- 4. Summary**

Plasma-Induced Damage (PID)

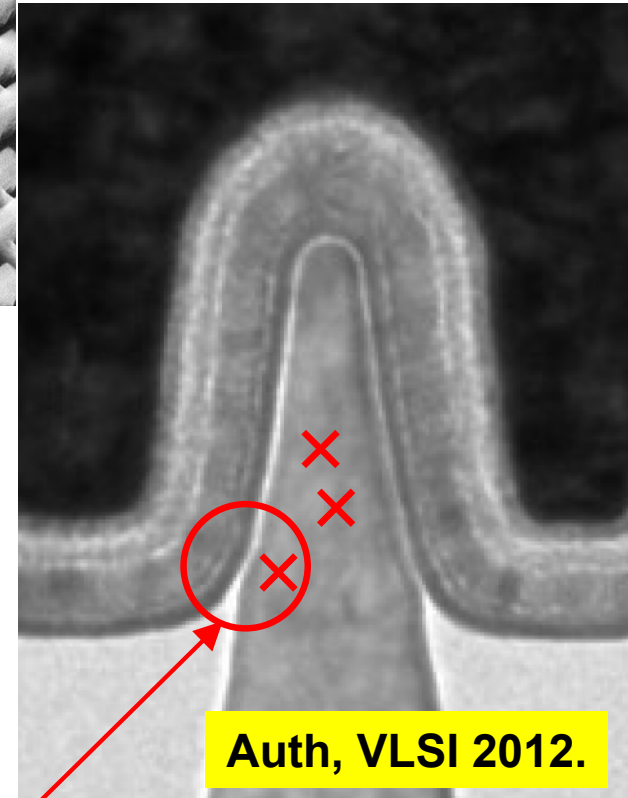
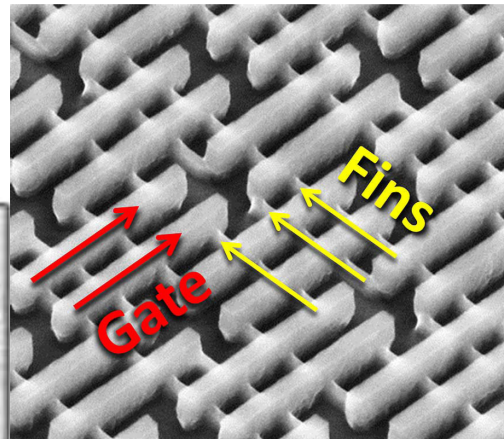
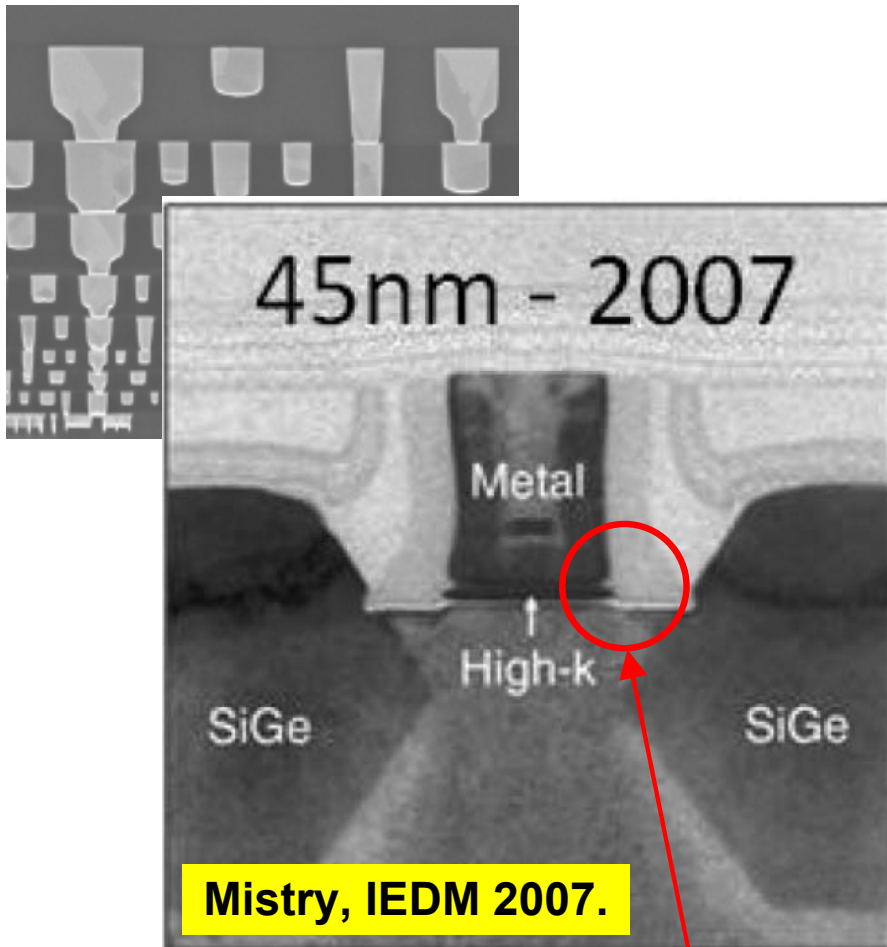


Performance
Reliability
Yield
Variability



PID naturally does not scale!!

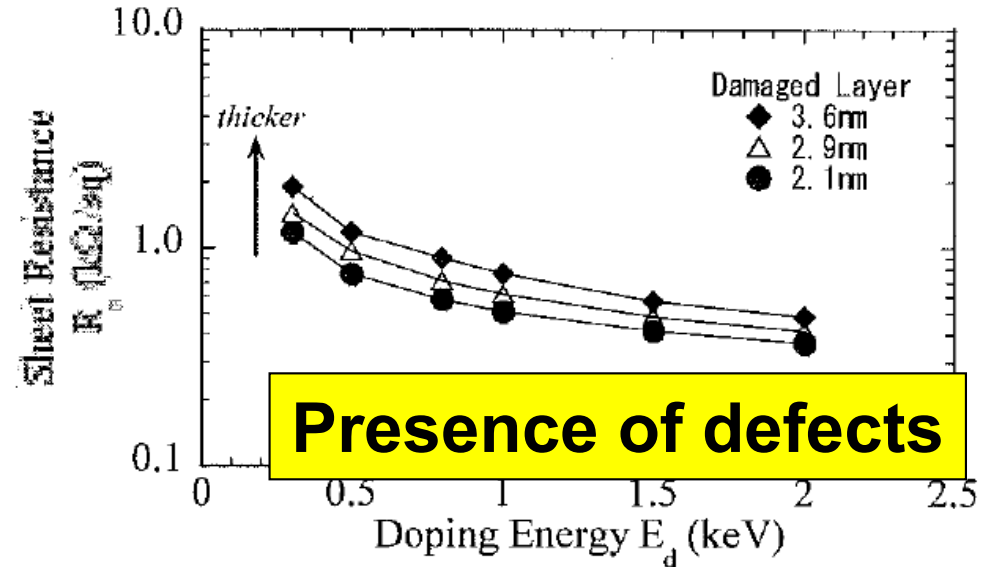
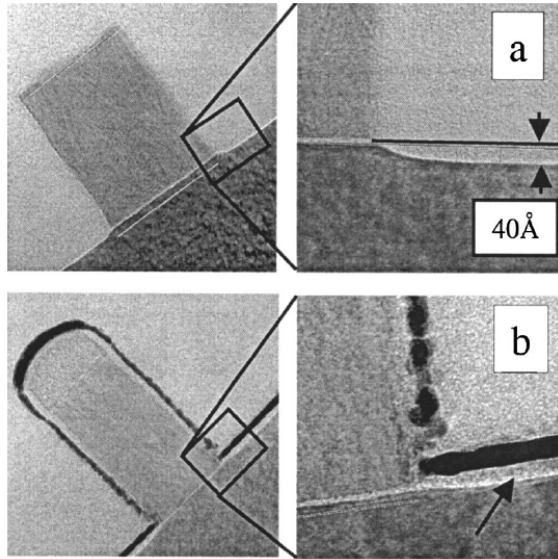
PID behind the scaling



Plasma-Induced Physical Damage (PPD)

Power consumption increase & Operating speed down!!

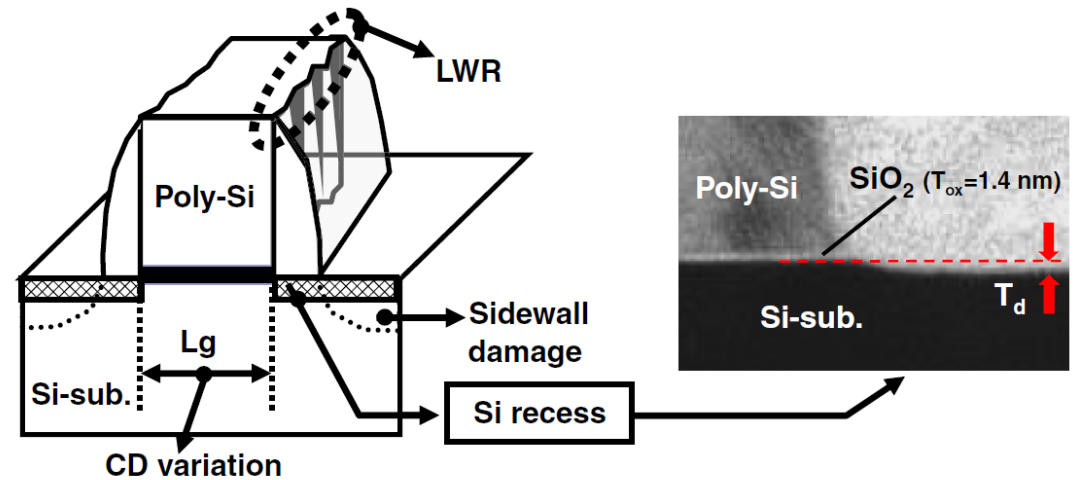
Previous reports on PID



S. A. Vitale and B. A. Smith: JVST B 21 (2003) 2205.

H. Kokura et al., Proc. Symp. Dry Process, 2005, p. 27.

Poly-Si Profile		
Etching Step	(a) Before OE (after ME)	(b) After OE
Si recess	No-Si recess 0.0nm	1.9nm



N. Yasui et al.: Proc. Symp. Dry Process (2007) 195.

T. Ohchi et al.: Jpn. J. Appl. Phys. 47 (2008) 5324.

Outline

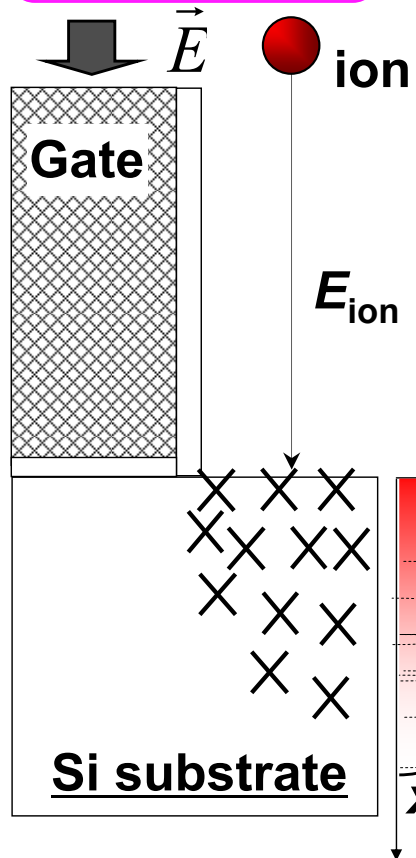
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PID Range Theory – Planar Device



Project Range → Stopping Power

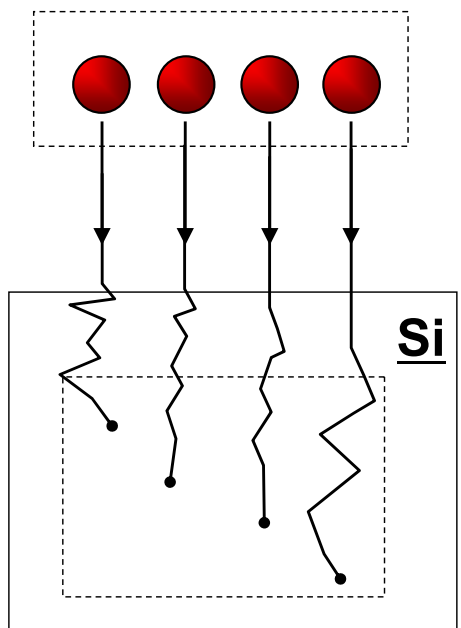
Plasma
(n_e, T_e, n_{ion})



$$-\frac{dE_{ion}}{dx} = n_0 S_d(E_{ion}) = n_0 \int_0^{\infty} T(E_{ion}, p) \cdot 2\pi p dp$$

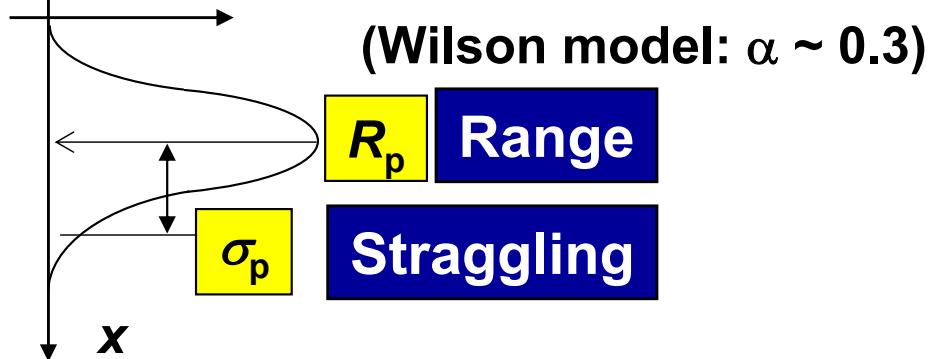
T: energy transfer
p: impact parameter

Potential-model-dependent



low energy limit ϵ : reduced energy

$$s_n(\epsilon) \rightarrow 0.5B\epsilon^{1-\alpha}$$

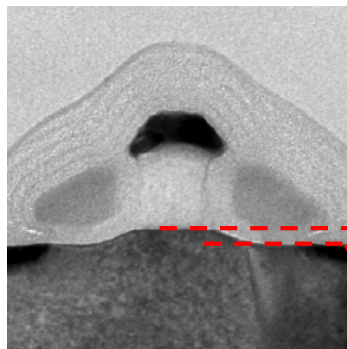
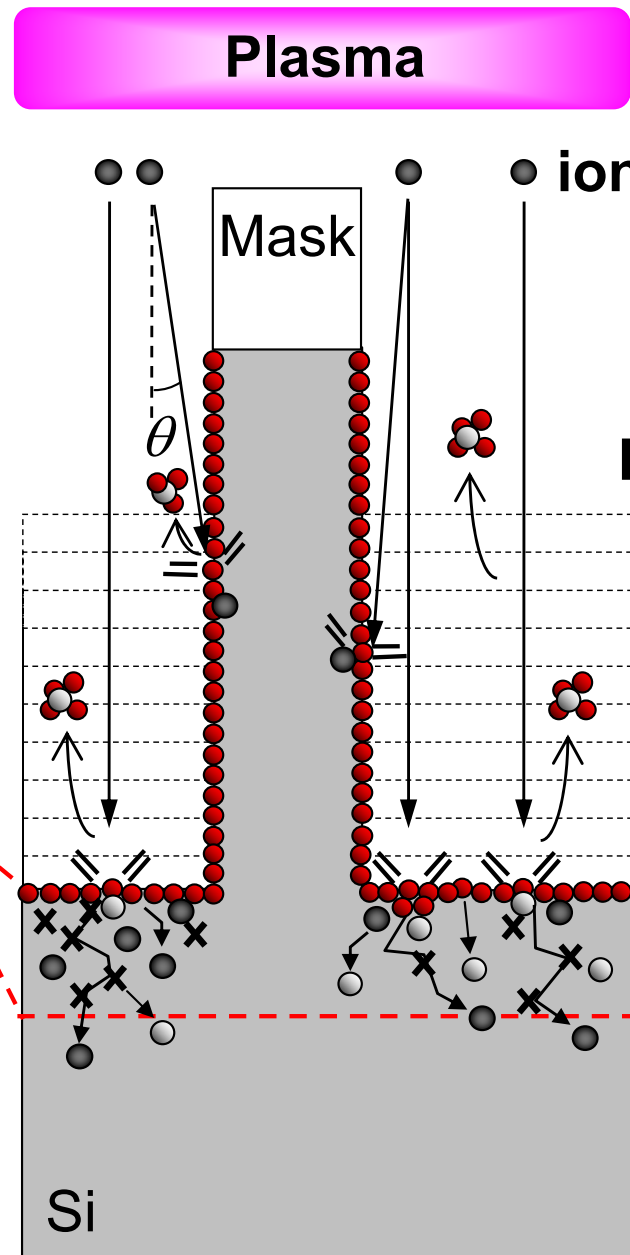


N. Bohr: Mat. Fys. Medd. K. Dan. Vidensk. Selsk. 18 (1948).
 J. Lindhard et al.: Mat. Fys. Medd. K. Dan. Vidensk. Selsk. 33, 1 (1963).
 G. Moliere: Z. Naturforschung A2, 133 (1947).
 W. D. Wilson et al.: Phys. Rev. B 15, 2458 (1977).

PID Range Theory – Planar Device



- ion
- Si
- radical



ex. Si Recess

Damaged layer

$$R_p = A_{ion} \cdot (E_{ion})^\alpha$$

(Eriguchi et al.)

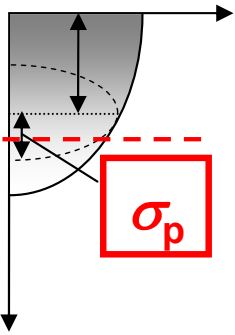
$$\sigma_p = \sqrt{\frac{2}{3}} \frac{\sqrt{M_{Si} M_{ion}}}{M_{Si} + M_{ion}} R_p$$

(LSS Theory)

Etching



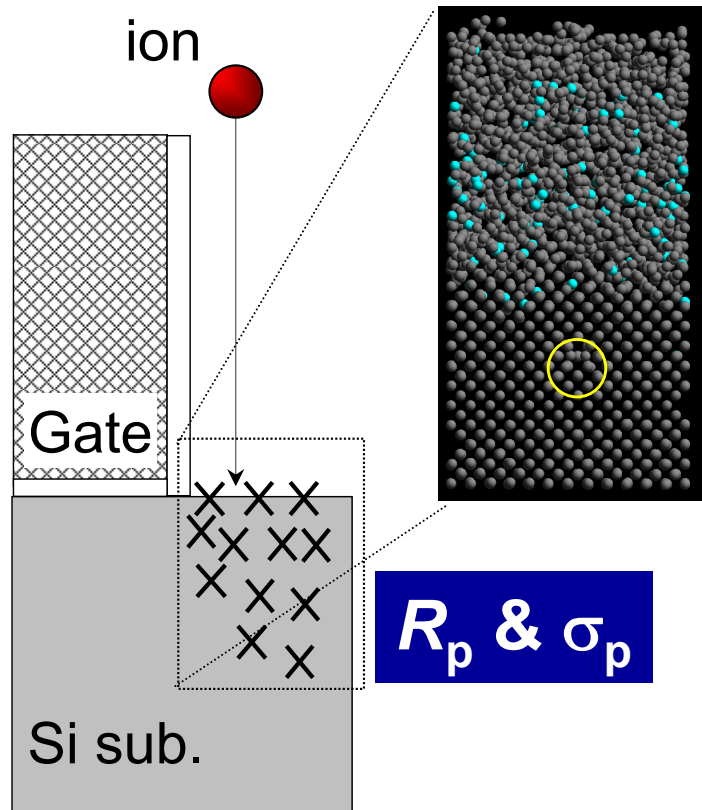
$$R_p$$



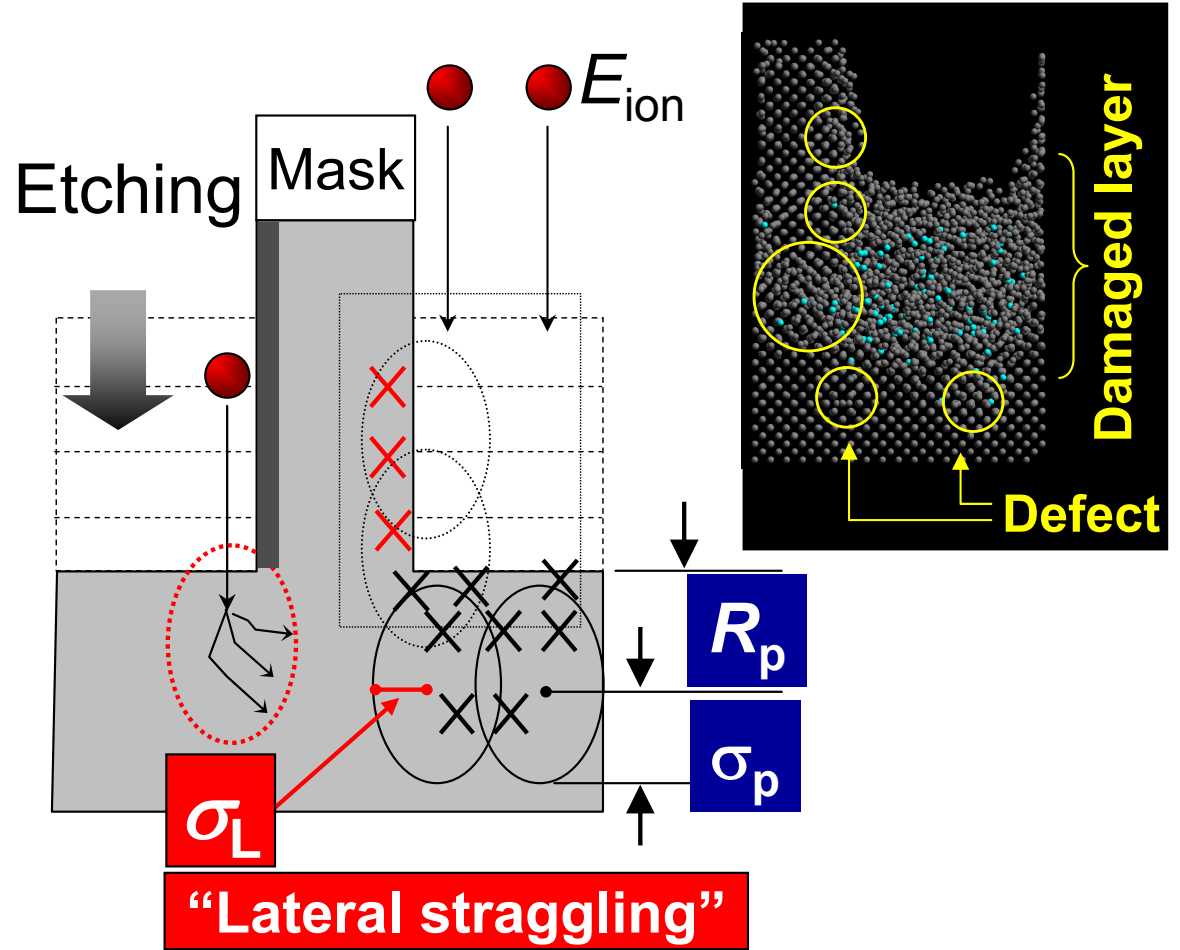
$$d_v \rightarrow R_p + \sigma_p$$

PID Range Theory – "Stragglings"

Plasma

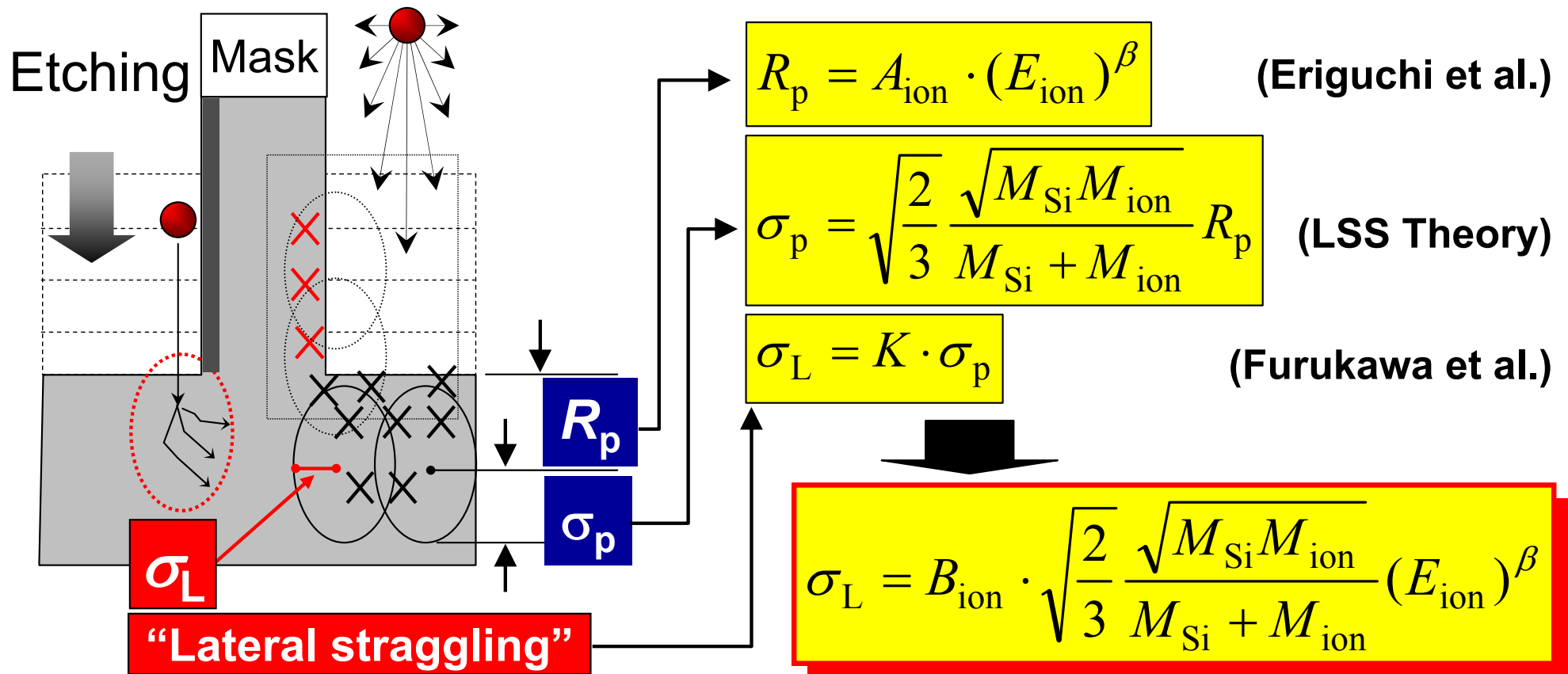


(a) Planar (1D)



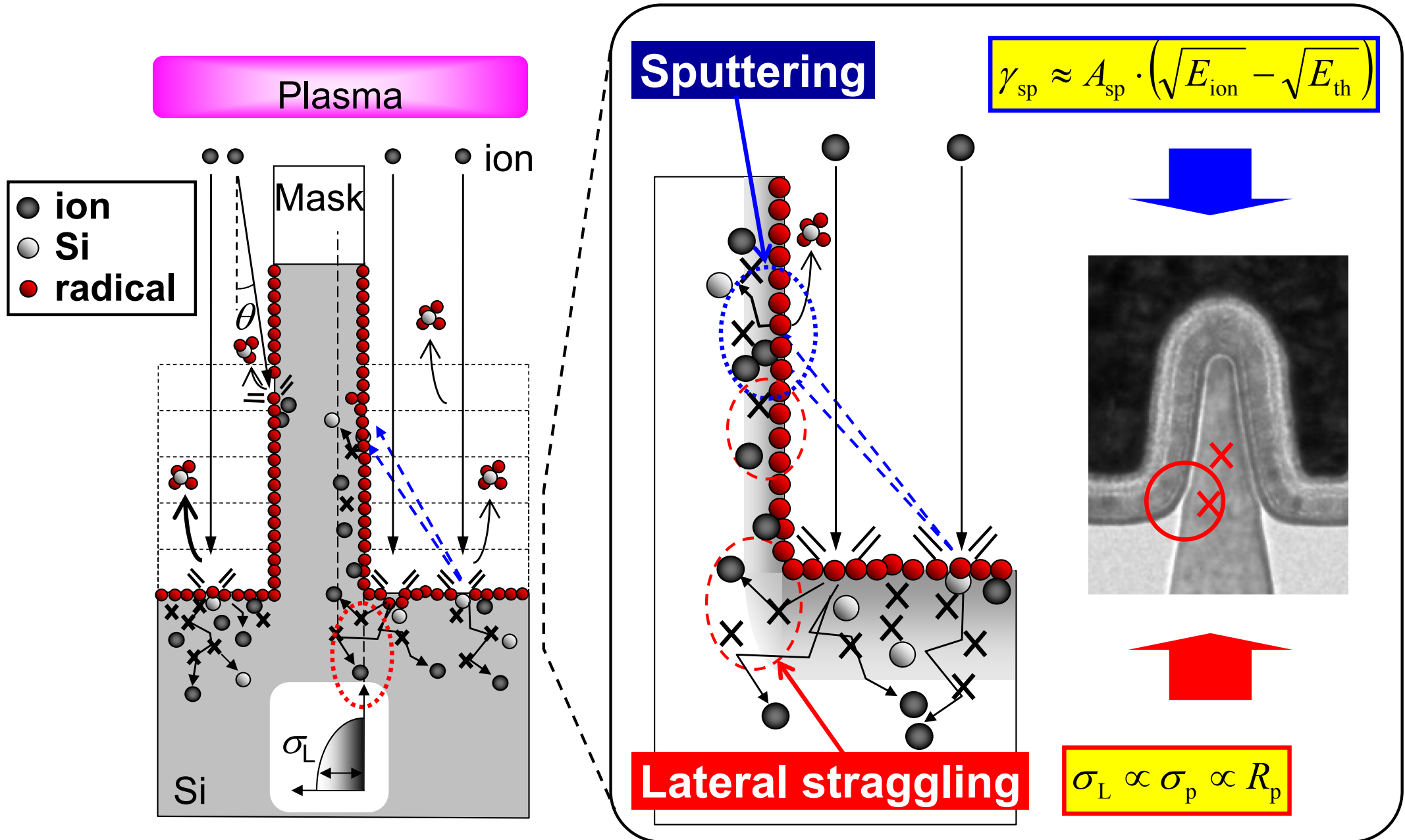
(b) Fin-structured (3D)

PID Range Theory – "Stragglings"



- (1) Lateral stragglings depends on M_{ion} , E_{ion} , and Si-ion potential.
- (2) Sidewall etching mechanism is governed, not only by direct ion impact & deposition, but also **by THIS STRAGGLING, σ_L !**
→ Damaged layer thickness $\sim R_p + \sigma_p$ (planar), σ_L (3D)

PID Model – 3D Device



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Simulation scheme



Classical Molecular Dynamics

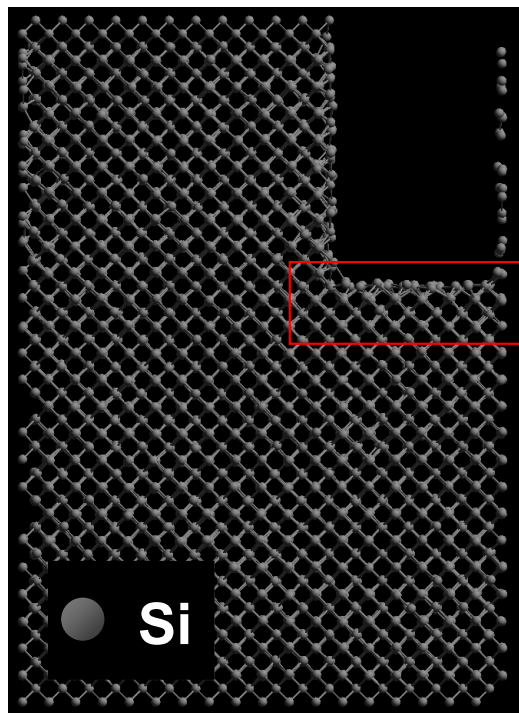
(Ohta and Hamaguchi, JVST 2001.)

Si–Si, Cl, O: Stillinger–Weber

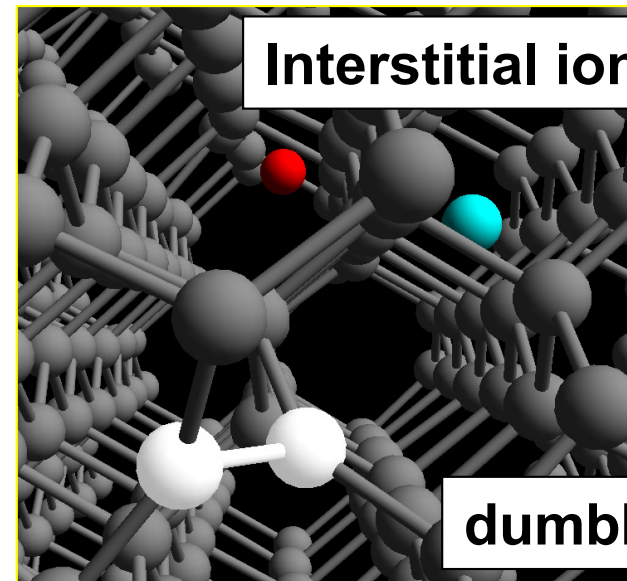
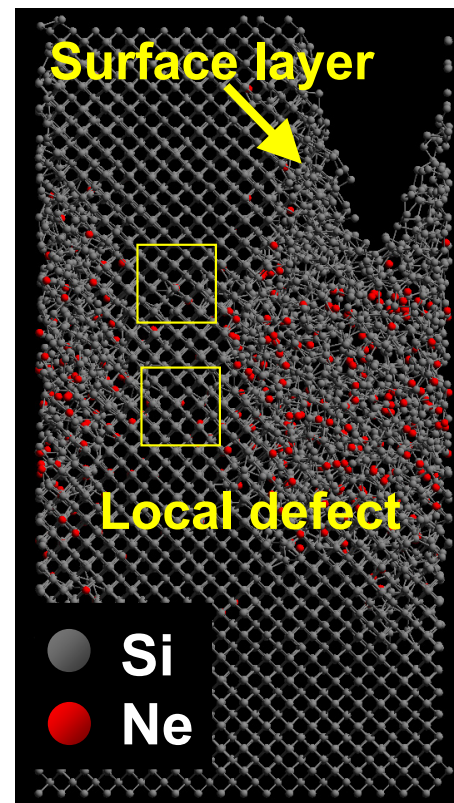
Phys. Rev. B, Vol.31, No.8, (1985), pp. 5262-5271

Noble gases: Wilson et al.

Phys. Rev. B, Vol.15, No.5, (1977), pp. 2458-2468



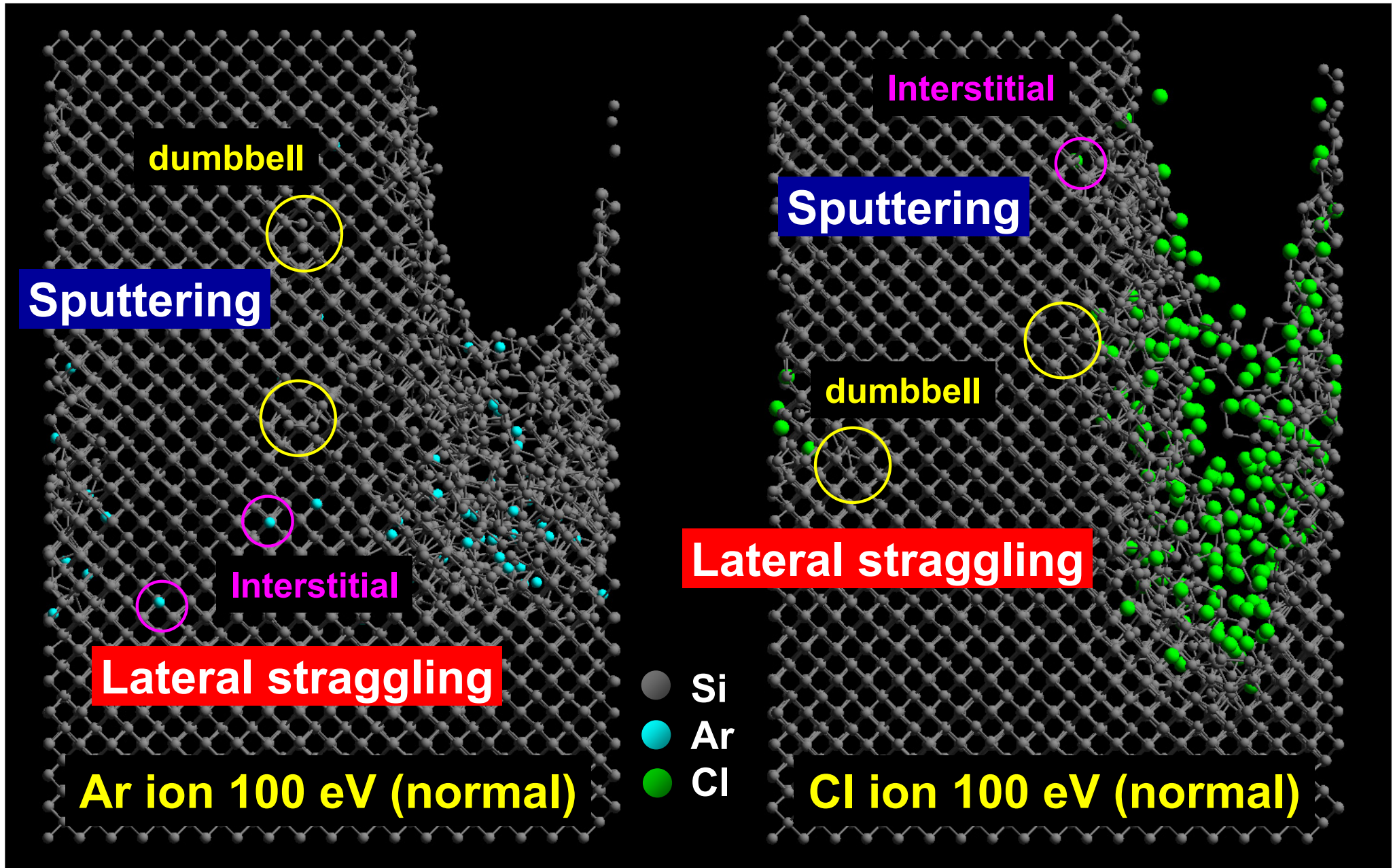
Starting material



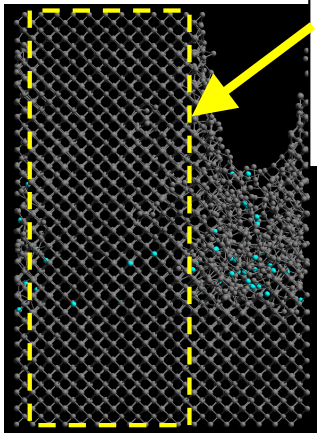
Defect analysis by

Density Functional Theory

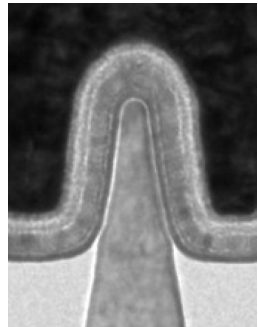
MD prediction results in Fin



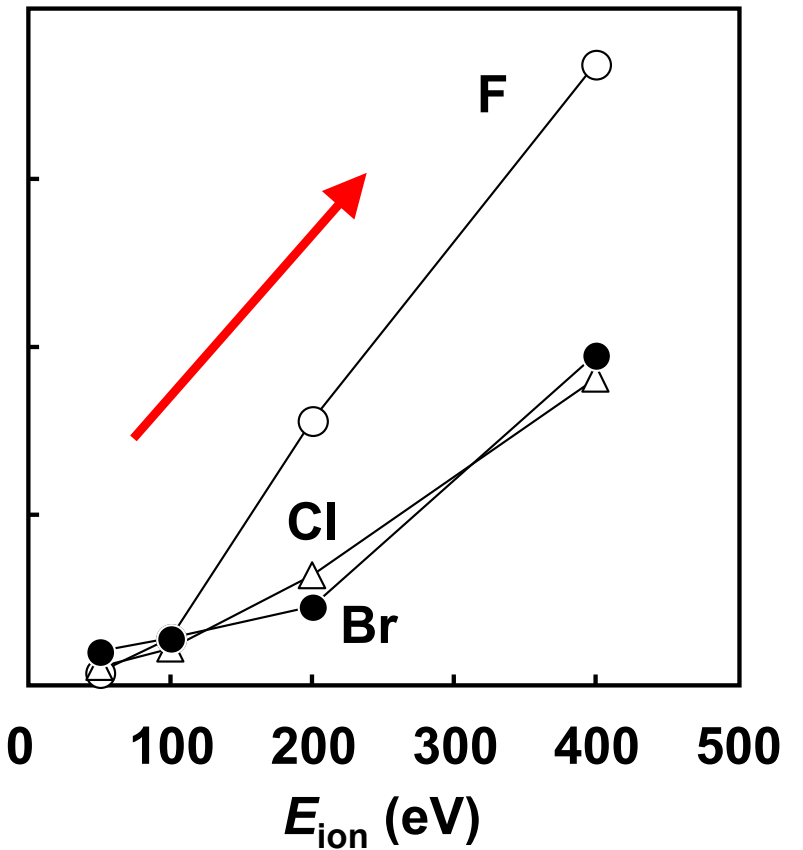
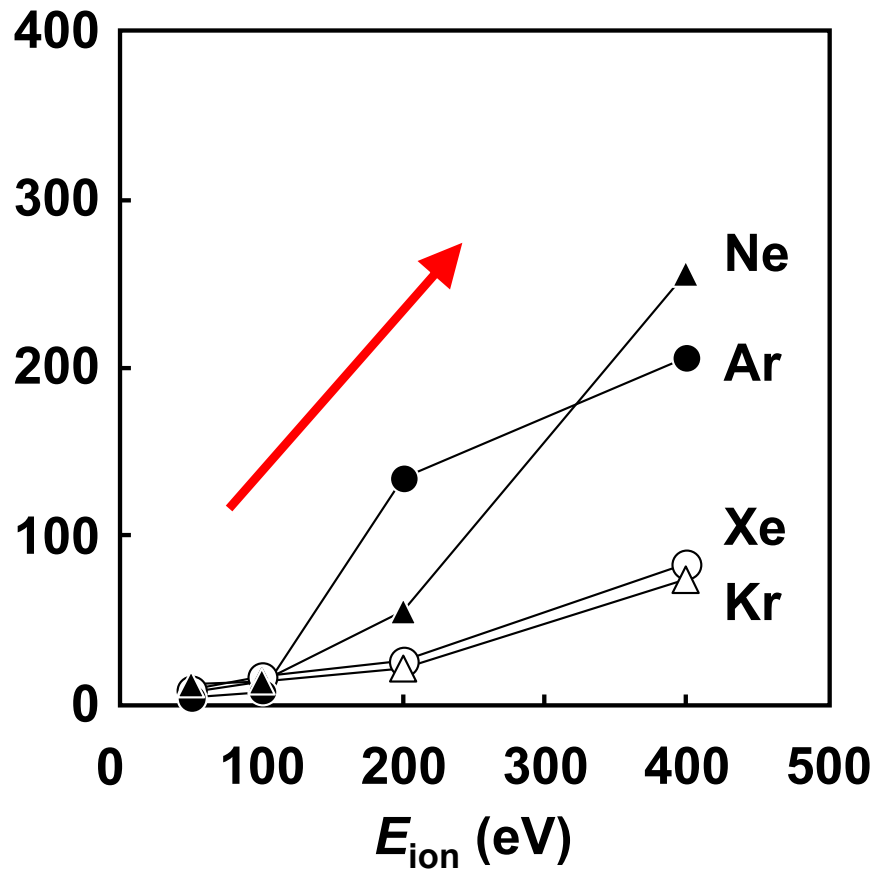
Defect creation in Fin



Counting the defects in this region one by one in accordance with the bond order and length.

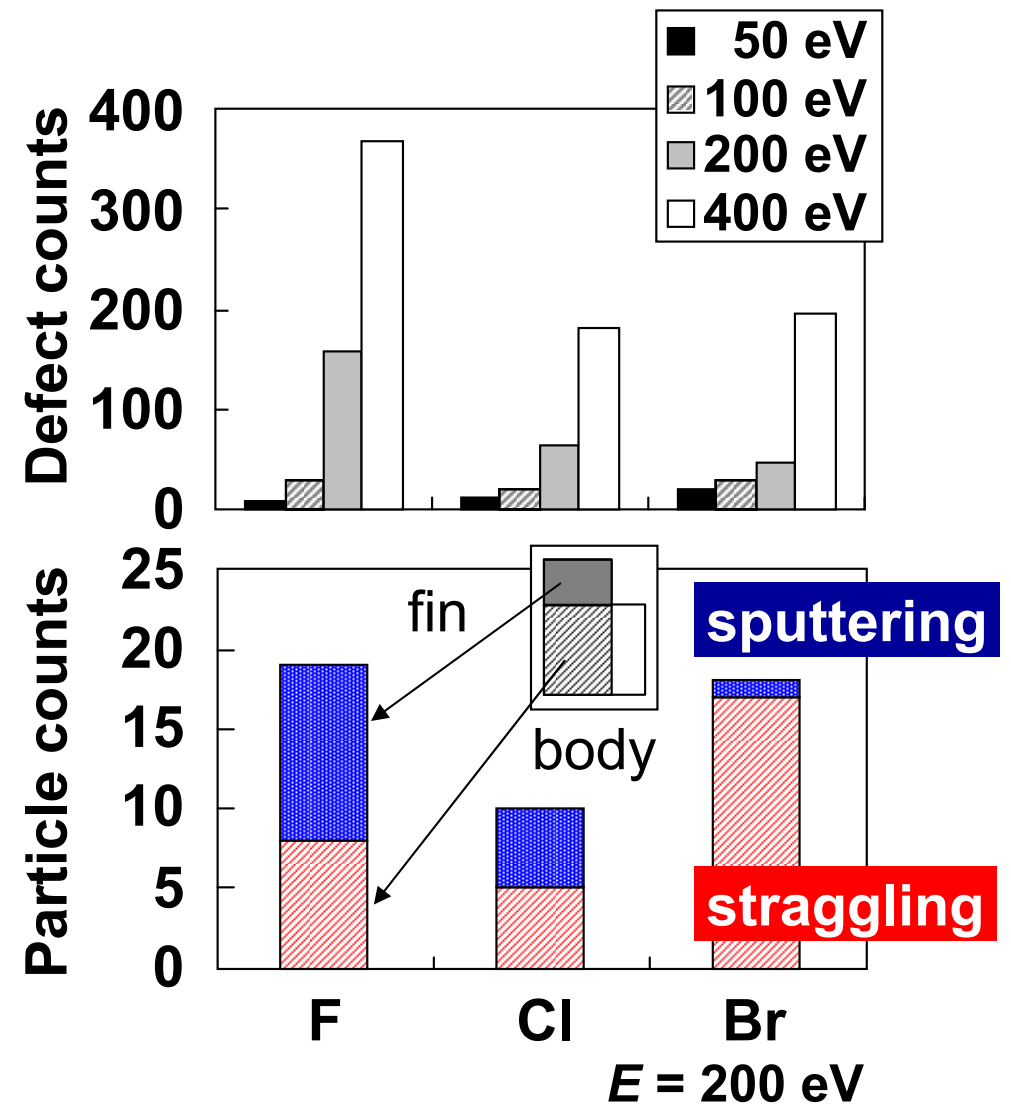
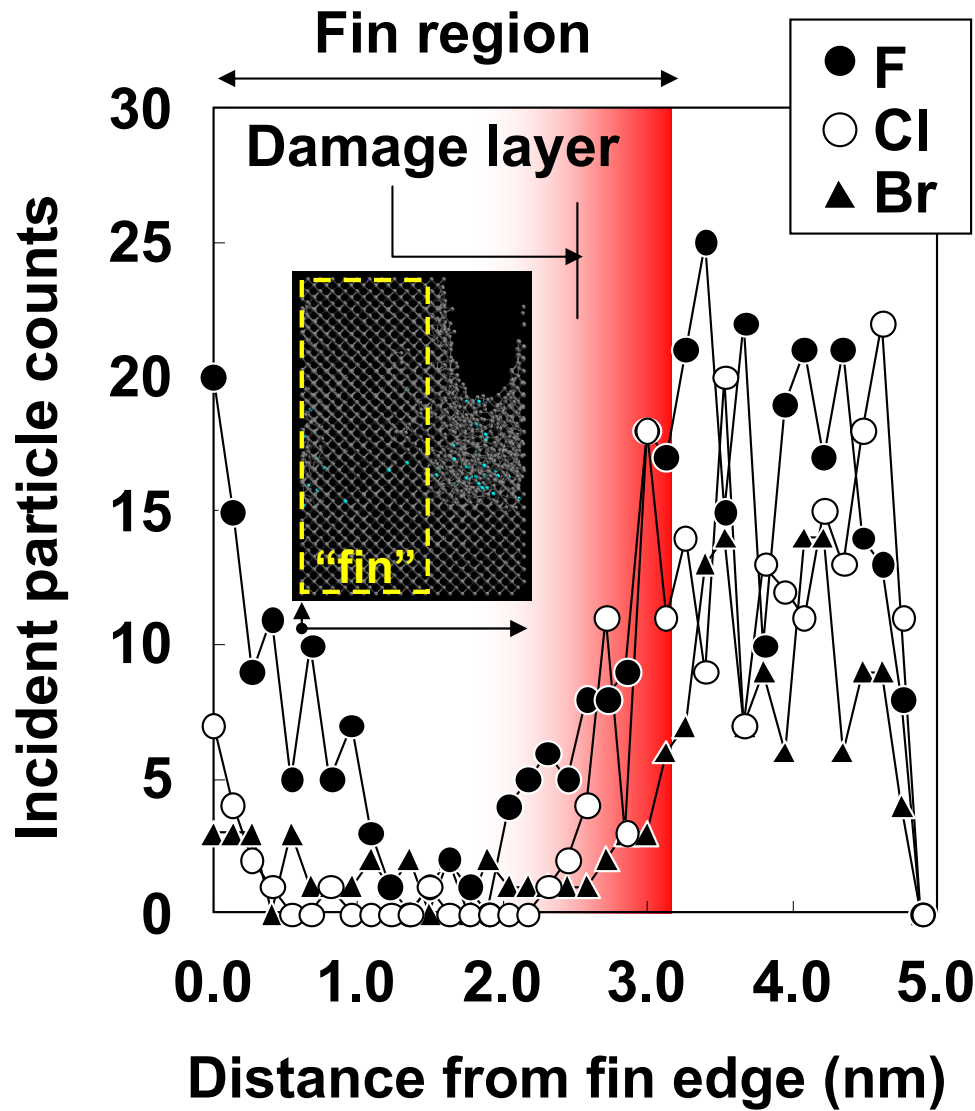


Counts



An ion with lighter mass and higher incident energy \rightarrow larger damage

Defect creation in "Fin"



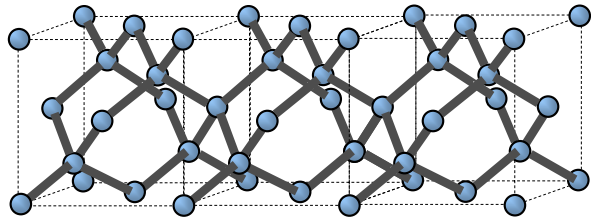
Both "sputtering" and "stragglings" are responsible for PID in 3D.

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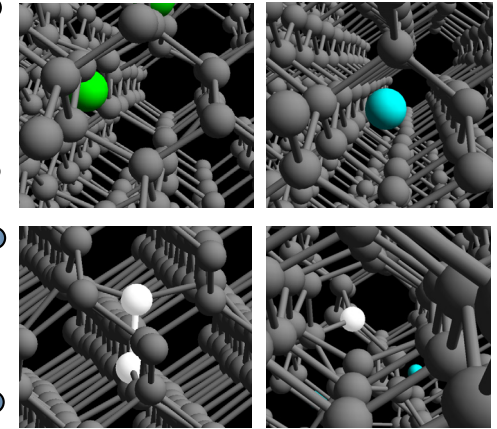
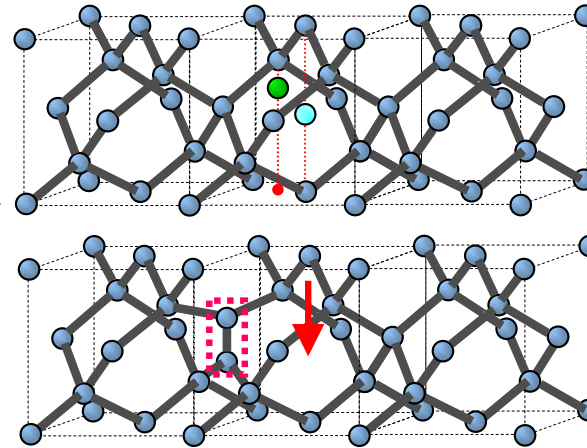
Electronic structure of defect

Original super-lattice structure

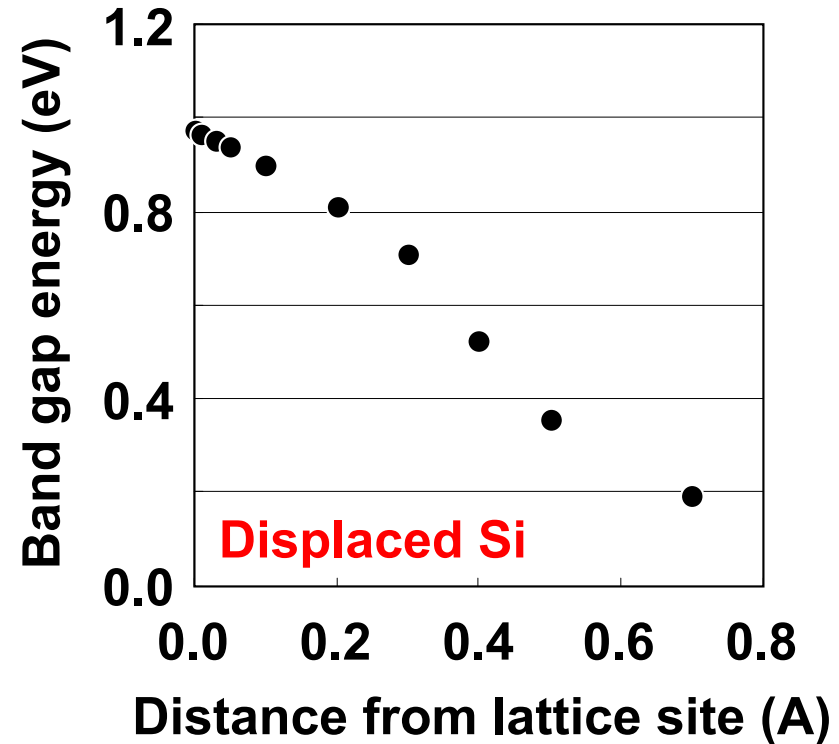
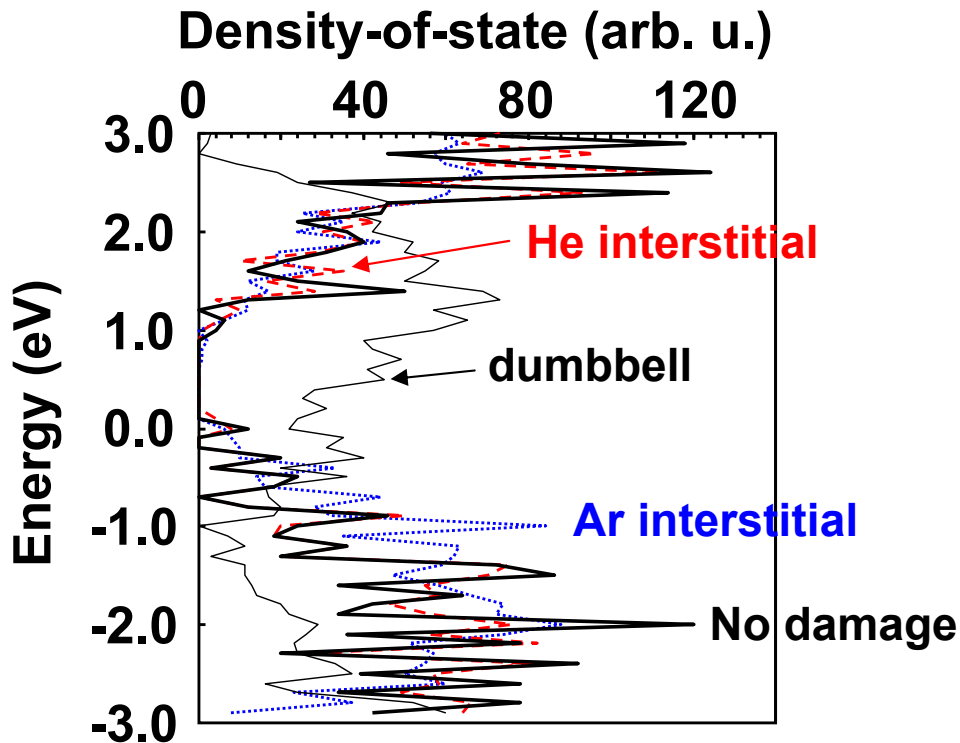


DFT: 6-31G with PBE/PBE & PBC

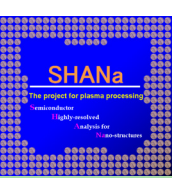
Damage



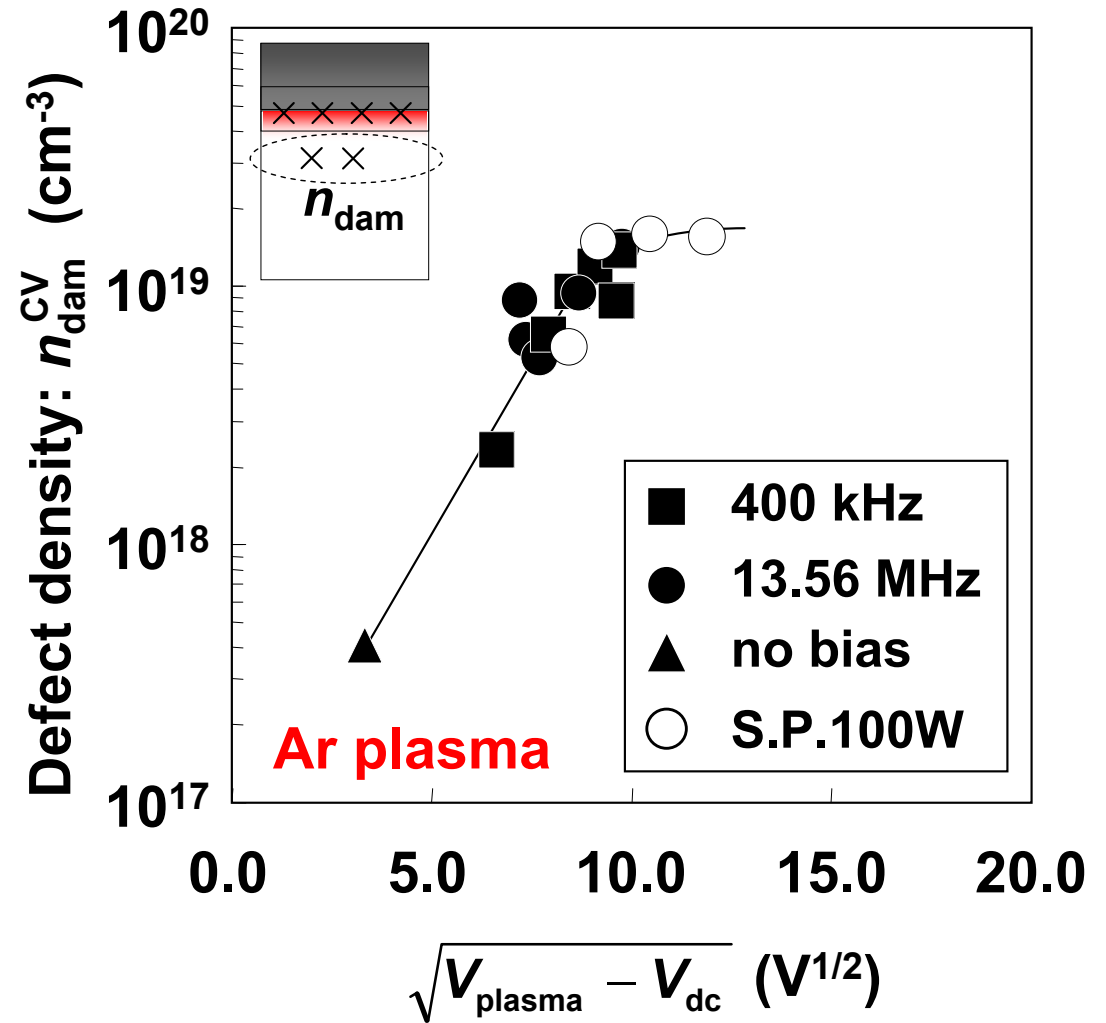
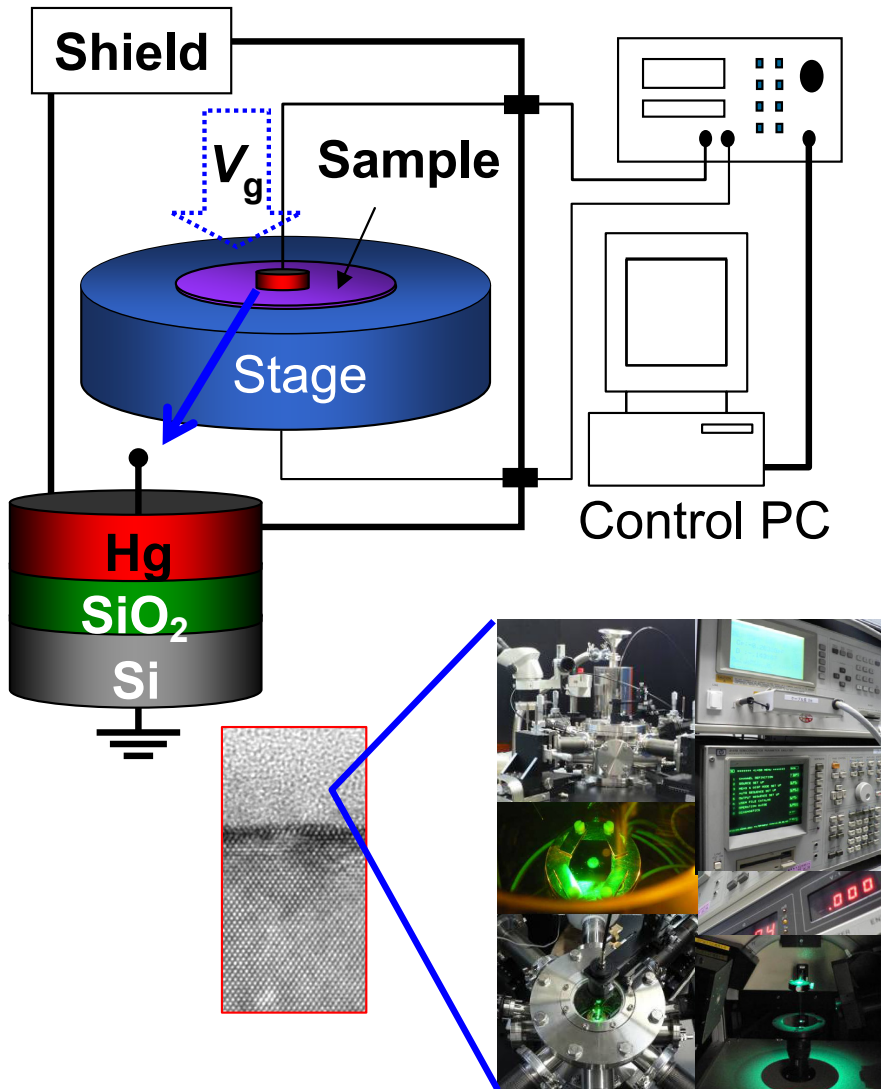
Displaced Si



Experimental evidence



Eriguchi IEDM 2008 / Kamei Thin Solid Films 518 (2010) / Nakakubo AVS 2011



Defect Density ~ 10¹⁸ - 10¹⁹ cm⁻³

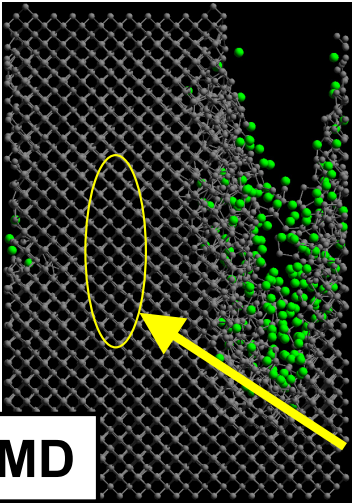
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PID prediction in Fin-structure

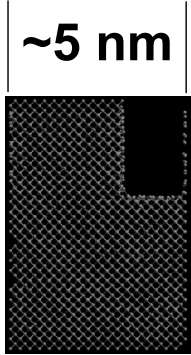
Assumption:

- (1) PID is modeled by the present scheme.
- (2) Surface damaged layer was stripped off.
- (3) Latent-defect creation was uncorrelated.



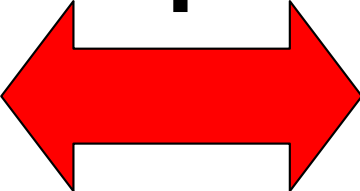
MD

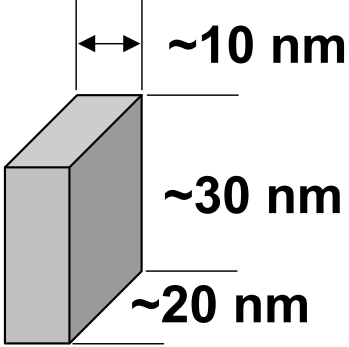
One in ~ every two snapshots



~5 nm

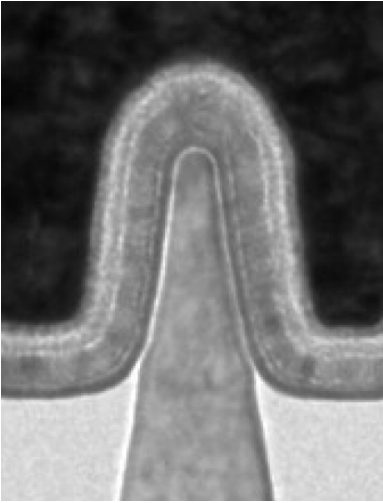
?





~10 nm
~30 nm
~20 nm

Typical fin size



$$P(n_{MD} > 0) = \frac{n_{dam}^{n_{MD}} \cdot \exp(-n_{dam})}{n_{MD}!}$$

$$n_{dam}(x)dx = \int_x^\infty \eta(E_{ion}) \cdot n_{ion} dy$$

Typical PID:
Ar 200 eV
 $n_p \sim 10^{11} \text{ cm}^{-3}$

Experiment in planar structure

$n_{dam} \sim 10^{18} - 10^{19} \text{ cm}^{-3}$

Present Models

$$\sigma_L = B_{ion} \cdot \sqrt{\frac{2}{3}} \frac{\sqrt{M_{Si} M_{ion}}}{M_{Si} + M_{ion}} (E_{ion})^\beta$$

$$\gamma_{sp} \approx A_{sp} \cdot (\sqrt{E_{ion}} - \sqrt{E_{th}})$$

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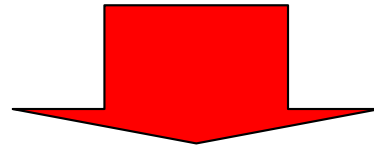
Summary



Plasma-induced damage in 3D structures were discussed.

- (1) A new PID model was proposed on the basis of**
 - (A) lateral straggling at the etched surface and**
 - (B) bombardment of sputtered species at the sidewall.**

- (2) A model prediction and MD simulations suggest that both the lateral straggling and the sputtered particle bombardment will become responsible for PID in scaled 3D structures.**



One should revise the views of plasma etching at the sidewall because the lateral PID is no longer negligible in ultimately scaled 3D devices.